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**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR**  
(AUTONOMOUS)

**B.Tech II Year II Semester Supplementary Examinations October-2020**

**ELECTRONIC CIRCUIT ANALYSIS**

(Electronics & Communication Engineering)

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

**UNIT-I**

- 1 a With neat diagram, derive the CE amplifier parameters using approximate analysis. **6M**  
b Obtain the expressions for current gain, voltage gain, input impedance and output impedance of CB amplifier using simplified hybrid model. **6M**

**OR**

- 2 Design a single stage RC coupled BJT amplifier for the following values. Assume that for Silicon transistor,  $V_{cc} = 10V$ ,  $I_C = 4mA$ ,  $h_{fe} = 100$ ,  $h_{ie} = 1K\Omega$ ,  $R_L = 100k\Omega$  and  $f_L = 100Hz$ . **12M**

**UNIT-II**

- 3 a Mention the typical values of Hybrid- $\pi$  parameters. **6M**  
b A BJT has the following parameters.  $g_m = 38m\Omega$ ,  $r_{b'e} = 5.9K\Omega$ ,  $h_{ie} = 6K\Omega$ ,  $r_{bb'}$  =  $100\Omega$ ,  $C_{b'c} = 12pF$ ,  $C_{b'e} = 63pF$  and  $h_{fe} = 224$  at 1 KHZ. Calculate  $f_{\alpha}$ ,  $f_{\beta}$  and  $f_T$  cutoff frequencies. **6M**

**OR**

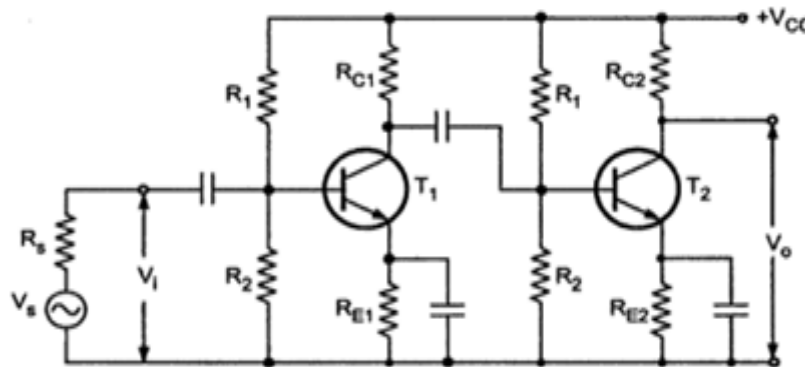
- 4 a Describe the relationship between low frequency h-parameters and high frequency Parameters. **8M**  
b Write about Collector junction capacitance and Emitter junction capacitance of hybrid- $\pi$  model. **4M**

**UNIT-III**

- 5 a What is Darlington Connection? Mention the advantages of Darlington Pair Amplifier. **4M**  
b With diagram, derive the expression for current gain and input resistance of Darlington amplifier. **8M**

**OR**

- 6 The following figure shows CE-CE cascade amplifier with their biasing arrangements. Calculate  $R_i$ ,  $A_i$ ,  $A_v$ ,  $R_i'$ ,  $A_{vs}$  and  $A_{is}$  if circuit parameters are:  $R_s = 1K$ ,  $R_{c1} = 15K$ ,  $R_{E1} = 100\Omega$ ,  $R_{C2} = 4 K\Omega$ ,  $R_{E2} = 330\Omega$  with  $R_1 = 200K$  and  $R_2 = 20K$  for first stage and  $R_1 = 47K$  and  $R_2 = 4.7K$  for second stage. Assume that  $h_{ie} = 1.2k\Omega$ ,  $h_{fe} = 50$ ,  $h_{re} = 2.5 \times 10^{-4}$  and  $h_{oe} = 25 \times 10^{-6} A/V$ . **12M**



**Fig. CE-CE Cascade amplifier**

**UNIT-IV**

- 7 a Discuss Feedback topologies **6M**  
b Derive the expressions of input and output resistances for Voltage Series FBA. **6M**

**OR**

- 8 a Classify the different types of oscillators **4M**  
b With neat diagram, explain Hartley Oscillator and derive the expression for frequency of oscillation **8M**

**UNIT-V**

- 9 a Write notes on Class AB operation **6M**  
b Discuss the need of Heat sink for power transistors. Mention about thermal stability of power transistors **6M**

**OR**

- 10 a Explain the effect of cascading single tuned amplifiers on bandwidth. **6M**  
b The bandwidth of single tuned amplifier is 20KHz. Calculate the bandwidth if such three stages are cascaded. Also calculate the bandwidth for four stages. **6M**

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